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Docket No. 0756-2415

Receipt

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

#4

In re Patent Application of )  
Hisashi OHTANI et al )  
Serial No. 10/026,802 )  
Filed: December 27, 2001 )  
For: METHOD FOR )  
MANUFACTURING A )  
SEMICONDUCTOR DEVICE )

Attention: Applications Branch

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April Campbell

Attention: Application Processing Division

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Washington, D.C. 20231

Sir:

The Filing Receipt received in the above-identified application has errors thereon due to  
Patent Office error as shown in red on the attached copy.

In view of the above, it is requested that a Corrected Filing Receipt be issued.

Respectfully submitted,

Eric J. Robinson

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APPLICATION NUMBER	FILING DATE	GRP ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLAIMS	IND CLAIMS
10/026,802	12/27/2001	2812	824	740756-2415	15	20	4

CONFIRMATION NO. 1815

22204  
NIXON PEABODY, LLP  
8180 GREENSBORO DRIVE  
SUITE 800  
MCLEAN, VA 22102

## FILING RECEIPT



\*OC000000007395860\*

Date Mailed: 01/31/2002

Receipt is acknowledged of this nonprovisional Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Customer Service Center. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

## Applicant(s)

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Takeshi Fukunaga, Atsugi-shi, JAPAN;  
Hongyong Zhang, Yamato-shi, JAPAN;

## Domestic Priority data as claimed by applicant

THIS APPLICATION IS A DIV OF 08/633,307 04/15/1996 PAT 6,335,541  
WHICH IS A DIV OF 08/329,644 10/25/1994 PAT 5,643,826

## Foreign Applications

JAPAN 5-294633 10/29/1993  
JAPAN 5-303436 11/09/1993  
JAPAN 5-307206 11/12/1993  
JAPAN 5-162705 06/20/1994

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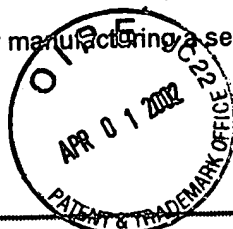
## Title

Method for manufacturing a semiconductor device

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## Preliminary Class

999

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Bib Data Sheet

CONFIRMATION NO. 1815

<b>SERIAL NUMBER</b> 10/026,802	<b>FILING DATE</b> 12/27/2001	<b>CLASS</b> 999	<b>GROUP ART UNIT</b> 2811	<b>ATTORNEY DOCKET NO.</b> 740756-2415
<b>RULE</b>				

## APPLICANTS

Hisashi Ohtani, Isehara-shi, JAPAN;  
 Akiharu Miyanaga, Hadano-shi, JAPAN;  
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\*\* CONTINUING DATA \*\*\*\*\* *Jm*

THIS APPLICATION IS A DIV OF 08/633,307 04/15/1996 PAT 6,335,541  
 WHICH IS A DIV OF 08/329,644 10/25/1994 PAT 5,643,826

\*\* FOREIGN APPLICATIONS \*\*\*\*\* *Jm*

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 JAPAN 5-307206 11/12/1993  
 JAPAN 6-162705 06/20/1994

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\*\* 01/30/2002

Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY JAPAN	SHEETS DRAWING 15	TOTAL CLAIMS 20	INDEPENDENT CLAIMS 4
35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance				
Verified and Acknowledged	Examiner's Signature <i>Jm</i>	Initials <i>Jm</i>		

## ADDRESS

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## TITLE

Method for manufacturing a semiconductor device

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